Project <Project Name>

<System Name>

Software Configuration Items Summary Table (SCIST) for [TBD]

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| --- | --- | --- | --- | --- | --- | --- | --- |
| CDRL: |  | Spec Refs: |  |  |  |  |  |
| <CDRL> |  | <Spec Refs> |  |  |  |  |  |
| Prepared: |  | <Prepared Name> |  |  |  | signed: |  |
|  |  | <Prepared Title> |  | Signature |  | yyyy-mm-dd |  |
| Verified: |  | <Verified Name> |  |  |  | signed: |  |
|  |  | <Verified Title> |  | Signature |  | yyyy-mm-dd |  |
| Approved: |  | <Approved Name> |  |  |  | signed: |  |
|  |  | <Approved Title> |  | Signature |  | yyyy-mm-dd |  |
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|  | Revision: |  | <Revision Level> |  |
|  | Status: |  | Draft |  |
|  | Date (yyyy-mm-dd): |  | 2011-08-04 |  |

Revision History

|  |  |  |  |
| --- | --- | --- | --- |
| Revision | Date  (yyyy-mm-dd) | Initials | Description of Changes |
|  |  |  | You may want to put „First issue“ here for the first version of the document |
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Table of Contents

[1 Introduction 4](#_Toc300222972)

[1.1 Purpose 4](#_Toc300222973)

[1.2 Abbreviations, Acronyms, and Definitions 4](#_Toc300222974)

[1.3 References 5](#_Toc300222975)

[2 Software Configuration Items Summary Table 6](#_Toc300222976)

[3 Software Configuration Items Development Environment Summary Table 9](#_Toc300222977)

[Appendix A: [TBD Title of this Appendix] 12](#_Toc300222978)

List of Figures

There are no figures in this document

List of Tables

[Table 1: List of Acronyms and Definitions 4](#_Toc300222964)

[Table 2: List of References 5](#_Toc300222965)

[Table 3: Software Configuration Items Summary Table 6](#_Toc300222966)

[Table 4: Software Configuration Items Development Environment Summary Table 9](#_Toc300222967)

# Introduction

[TBD – Make sure you have the correct master template (ANNAX Template for Technical Documents.dotm) correctly installed in the correct location (see hints in the master template).

Please evoke the “Document Information” dialog by pressing **SHIFT+F10** in order to enter document information like e.g. document title and document number.

This SCIST template contains **explanatory hidden text**. Pressing **ALT+H** will **toggle on/off** hidden text. Please remove this paragraph as well as all hidden text in the document before or when you finalize the SCIST.]

Removal of hidden text is supported by a Word macro: Pressing SHIFT+F10 will evoke a dialog. Clicking „Finalize document“ will remove all hidden text automatically.

Please leave the structure of the SCIST unchanged. This will facilitate re-use of document content in future projects.

## Purpose

The following paragraph is only an example.

This Software Configuration Items Summary Table (SCIST) is applicable to the [TBD identification of what will be developed] software development for the [TBD project name] project. The purpose of this SCIST is to describe the details of the [TBD identification of what will be developed] Software Configuration Items (SCIs) and the related development environment.

## Abbreviations, Acronyms, and Definitions

The following acronyms and definitions are used within this SCIST.

Please add all acronyms and their definitions. Delete all acronyms not used in this document from the table before finalizing the document.

Table : List of Acronyms and Definitions

| Acronym | Definition |
| --- | --- |
| APP | Application |
| CPU | Central Processing Unit |
| IDE | Integrated Development Environment |
| IEEE | Institute of Electrical and Electronics Engineers |
| LRU | Line Replacement Unit |
| N/A | Not Applicable |
| PN | Part Number |
| OS | Operating System |
| SCI | Software Configuration Item |
| SCIST | Software Configuration Items Summary Table |
| SDD | Software Design Description |
| SFD | System Functional Description |
| SIL | Software Integrity Level |
| SRS | Software Requirements Specification |

## References

The following documents are referenced within this SCIST:

Please add all documents referenced in this document. Use the format [...] for the references.

Table : List of References

| Doc ID | Document Description |
| --- | --- |
| TBD |  |
| [I830] | IEEE Std 930-1998 - IEEE Recommended Practice for Software Requirements Specifications |
| [I1558] | IEEE Std 1558-2004 - IEEE Standard for Software Documentation for Rail Equipment and Systems |
|  |  |

# Software Configuration Items Summary Table

The following table is only an example, taken from Rev. 03 of the SCIST for the TTC project. Please adjust it to fit your purposes. You might want to check whether an update version of the SCIST for the TTC Toronto project ist available for reference (Doc No. 299017).

Column „SIL“: Delete this row if no „Software Integrity Level“ / „Software Safety Integrity Level“ requirement is stipulated by the customer

Column „Cat“: This relates to a category defined by the TTC customer. The category expresses the degree of modification of the SCI for the project. You might want to define your own categories, e.g. „COTS“ (Commercial-Off-The-Shelf), „Non-COTS“.

Table : Software Configuration Items Summary Table

| SCI # | SCI Identification | SIL | Cat. | Operation Environment | Requirements & Functional Description (References) | Line Replacement Unit (LRU) | Host Component Identification | Upload Method | Deliverable Medium |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| PSC | | | | | | | | | |
| AXPSC01 | PSC OS  Operating System  **License:** NO | 1 | C | **OS:** N/A  **License:** N/A | **SFD:** [ASFD]  **SRS:** [SRS\_PSC]  **SDD:** [SDD\_PSC] | **LRU Name:** PSC **PN:** 54010103S | CPU: MPC885CVR133 **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| AXPSC02 | PSC APP  Application  **License:** NO | 1 | B | **OS:** Linux (DENX), version: 2.6.22.6  **License:** YES (GPL) | **SFD:** [ASFD]  **SRS:** [SRS\_PSC]  **SDD:** [SDD\_PSC] | **LRU Name:** PSC **PN:** 54010103S | CPU: MPC885CVR133 **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| AXPSC03 | PSC DB  Database  **License:** NO | 1 | A | **OS:** Linux (DENX), version: 2.6.22.6  **License:** YES (GPL) | **SFD:** [ASFD]  **SRS:** [SRS\_PSC]  **SDD:** [SDD\_PSC] | **LRU Name:** PSC **PN:** 54010103S | CPU: MPC885CVR133 **Chip PN**: N/A **Board PN:** N/A | Through PTE and FMS/MCG | CD ROM |
| AXPSC04 | PSC DSPFW  DSP Firmware  **License:** NO | 1 | B | **OS:** N/A  **License:** NO | **SFD:** [ASFD]  **SRS:** [SRS\_PSC]  **SDD:** [SDD\_PSC] | **LRU Name:** PSC **PN:** 54010103S | Chip: Freescale DSP56303 **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| PCU-Box | | | | | | | | | |
| AXPCU01 | PCU-Box OS  Operating System  **License:** NO | 1 | C | **OS:** N/A  **License:** N/A | **SFD:** [ASFD]  **SRS:** [SRS\_PCUB]  **SDD:** [SDD\_PCUB] | **LRU Name:** PCU-Box **PN:** 53050130S | CPU: ARM 926 EJ, 210 MHz **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| AXPCU02 | PCU-Box APP  Application  **License:** NO | 1 | A | **OS:** Linux (Emlix), version: 2.6.27  **License:** YES (GPL) | **SFD:** [ASFD]  **SRS:** [SRS\_PCUB]  **SDD:** [SDD\_PCUB] | **LRU Name:** PCU-Box **PN:** 53050130S | CPU: ARM 926 EJ, 210 MHz **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| CCU | | | | | | | | | |
| AXCCU01 | CCU OS  Operating System  **License:** NO | 1 | C | **OS:** N/A  **License:** N/A | **SFD:** [ASFD]  **SRS:** [SRS\_CCU]  **SDD:** [SDD\_CCU] | **LRU Name:** CCU  **PN:** 84100290S | CPU: LPC3240 **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| AXCCU02 | CCU APP  Application  **License:** NO | 1 | A | **OS:** Linux (NXP), version: 2.6.27.8  **License:** YES (GPL) | **SFD:** [ASFD]  **SRS:** [SRS\_CCU]  **SDD:** [SDD\_CCU] | **LRU Name:** CCU  **PN:** 84100290S | CPU: LPC3240 **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| Displays | | | | | | | | | |
| AXDISP01 | DISP OS  **License:** NO | 1 | C | **OS:** N/A  **License:** N/A | **SFD:** [ASFD]  **SRS:** [SRS\_DISP]  **SRS:** [SDD\_DISP] | **LRU Name:** Display **PN:** 84170281S, 84170282S, 84170283S, 84170284S | CPU: ARM 926 **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| AXDISP02 | DISP APP  **License:** NO | 1 | B | **OS:** Linux, version: 2.6.31  **License:** YES (GPL) | **SFD:** [ASFD]  **SRS:** [SRS\_DISP]  **SRS:** [SDD\_DISP] | **LRU Name:** Display **PN:** 84170281S, 84170282S, 84170283S, 84170284S | CPU: ARM 926 **Chip PN**: N/A **Board PN:** N/A | Through PTE | CD ROM |
| Comm./PIS PTE | | | | | | | | | |
| AXPTE01 | PTE OS  Operating System  **License:** YES | 1 | C | **OS:** N/A  **License:** N/A | **SFD:** [ASFD]  **SRS:** [SRS\_PTE]  **SDD:** [SDD\_PTE] | **LRU Name:** (Bombardier SoS) **PN:** (Bombardier SoS) | CPU: Intel Core 2 Duo **Chip PN**: N/A **Board PN:** N/A | Installation from DVD ROM | DVD ROM |
| AXPTE02 | PTE APP  Application  **License:** YES | 1 | A | **OS:** Windows XP, version: SP2 or higher  **License:** YES | **SFD:** [ASFD]  **SRS:** [SRS\_PTE]  **SDD:** [SDD\_PTE] | **LRU Name:** (Bombardier SoS) **PN:** (Bombardier SoS) | CPU: Intel Core 2 Duo **Chip PN**: N/A **Board PN:** N/A | Installation from CD ROM | CD ROM |
| FMS | | | | | | | | | |
| AXFMS01 | FMS OS  Operating System  **License:** YES | 1 | C | **OS:** N/A  **License:** [N/A] | **SFD:** [ASFD]  **SRS:** N/A  **SDD:** N/A | **LRU Name:** (TTC SoS) **PN:** (TTC SoS) | CPU: (TTC SoS) **Chip PN**: (TTC SoS) **Board PN:** (TTC SoS) | Through FMS | DVD ROM |
| AXFMS02 | FMS APP  **License:** YES | 1 | C | **OS:** Windows XP Server, version: SP2 or higher  **License:** YES | **SFD:** [ASFD]  **SRS:** N/A  **SDD:** N/A | **LRU Name:** (TTC SoS) **PN:** (TTC SoS) | CPU: (TTC SoS) **Chip PN**: (TTC SoS) **Board PN:** (TTC SoS) | Through FMS | CD ROM |

# Software Configuration Items Development Environment Summary Table

The following table is only an example, taken from an Rev. 03 of the SCIST for the TTC project. Please adjust it to fit your purposes. You might want to check whether an update version of the SCIST for the TTC Toronto project ist available for reference (Doc No. 299017).

Table : Software Configuration Items Development Environment Summary Table

| SCI No. | SW Dev. Platform | IDE | Prog. Lang. | Compiler | Linker | Libraries | ICE | Build Proc. | Simulation Tools | Special Tools | Programming Tools |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| AXPSC01 | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| AXPSC02 | Windows XP Version 2002 Service Pack 3  **License:** YES | Eclipse  **License:** YES (GPL) | C++/C | GCC Suite 4.3.2  **License:** YES (GPL) | GCC Suite 4.3.2  **License:** YES (GPL) | N/A | N/A | Makefile | N/A | N/A | N/A |
| AXPSC03 | Windows XP Version 2002 Service Pack 3  **License:** YES | PASIS CE  **License:** YES | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| AXPSC04 | Windows XP Version 2002 Service Pack 3  **License:** YES | N/A | CLAS 563 | CLAS 563 | N/A | N/A | N/A | Makefile | N/A | N/A | N/A |
| AXPCU01 | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| AXPCU02 | Windows XP Version 2002 Service Pack 3  **License:** YES | Eclipse  **License:** YES (GPL) | C++/C | GCC Suite 4.3.2  **License:** YES (GPL) | GCC Suite 4.3.2  **License:** YES (GPL) | N/A | N/A | Makefile | N/A | N/A | N/A |
| AXCCU01 | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| AXCCU02 | Windows XP Version 2002 Service Pack 3  **License:** YES | Eclipse  **License:** YES (GPL) | C++/C | GCC Suite 4.3.2  **License:** YES (GPL) | GCC Suite 4.3.2  **License:** YES (GPL) | N/A | N/A | Makefile | N/A | N/A | N/A |
| AXDIP01 | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| AXDIP02 | Linux Fedora10  **License:** YES (GPL) | ptxdist 1.0.0 (1.0.0 based)  **License:** YES (GPL) | C++/C | GCC 4.1.2 (gnueabi)  **License:** YES (GPL) | LD 2.1  (gnueabi)  **License:** YES (GPL) | Boost, FreeType | N/A | Makefile | N/A | N/A | N/A |
| AXPTE01 | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| AXPTE02 | Windows XP Version 2002 Service Pack 3  **License:** YES | Eclipse  **License:** YES (GPL) | C++/C | GCC Suite 4.3.2  **License:** YES (GPL) | GCC Suite 4.3.2  **License:** YES (GPL) | N/A | N/A | Included in IDE | N/A | N/A | N/A |
| AXFMS01 | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| AXFMS02 | Windows XP Version 2002 Service Pack 3  **License:** YES | Microsoft Visual Studio 2008  **License:** YES | C++ | Included in IDE | Included in IDE | N/A | N/A | Included in IDE | N/A | N/A | N/A |
| AXBKF01 | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| AXBKF02 | Windows XP Version 2002 Service Pack 3  **License:** YES | Eclipse  **License:** YES (GPL) | Java SE 1.6.13 | Included in IDE | Included in IDE | Hibernate | N/A | Included in IDE | N/A | N/A | N/A |

Appendix A: [TBD Title of this Appendix]

Please adjust headline – or delete headline if no appendix is used.